

REMARKS

This application has been reviewed in light of the Office Action dated December 16, 2002. Claims 17 and 18 are presented for examination. Claim 17, which is in independent form, has been amended to define more clearly what Applicant regard as his invention. Applicant also notes that Claim 18 has been amended to change its dependency from previously canceled Claim 1 to pending Claim 17. Applicant notes that the change to Claim 18 affects matters of form only and does not, in any way, narrow the scope of Claim 18. Favorable reconsideration is requested.

The Office Action rejected Claims 17 and 18 under 35 U.S.C. § 112, second paragraph, as being incomplete, asserting that Claim 18 depends from canceled Claim 1, and for Claim 17, that there is no antecedent basis for the recitation "said frame memory unit." As described above, the dependency of Claim 18 has been changed to Claim 17. In addition, the recitation "said frame memory unit" has been deleted from Claim 17. Applicant believes that the rejections under Section 112, second paragraph, have been overcome and therefore, Applicants respectfully request their withdrawal.

The Office Action rejected Claim 17 under 35 U.S.C. § 112, first paragraph, as containing subject matter which was not described in the specification in such a way as to enable one skilled in the art to which it pertains, or with which it is most nearly connected, to make and/or use the invention. In particular, the Office Action at page 3 asserts that the application does not disclose what size is suitable for a first FIFO section for storing image data inputted. In response, Applicant has deleted from Claim 17 the recitation "wherein said first FIFO section is of a size suitable for storing image data inputted during a period that equals a sum of a period for reading image data from said

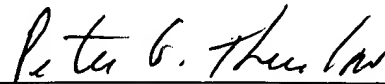
frame memory unit a plurality of times and a period necessary for command of said frame memory unit” and replaced it with the recitation --the image data is read out from said first FIFO section, written into said frame memory section, and read out from said frame memory section at a rate that is half of a rate at which the image data is inputted into said first FIFO section, and, within a period for inputting the image data into said first FIFO to FULL capacity, writing the image data into said frame memory section, a plural times of reading the image data from said frame memory section, and executing a command of said frame memory section are conducted--. Applicant submits that support in the specification for this recitation can be found at least from page 8, line 21, to page 10, line 19, and is shown in Figure 3. Referring to Figure 3, a period for 64 clocks would be necessary for writing an image data of a 64-bit width into a frame memory 1 (as shown in Figure 1), a plural (2) times of reading the image data written therein, and executing the command. Accordingly, if a period from a start of inputting the next image data into a first FIFO 3 (as shown in Figure 1), until the first FIFO 3 is written in its full capacity is at least a 64-clock period, the image data processing can be performed efficiently while controlling the frame memory.

Applicant gratefully acknowledges the indication that Claims 17 and 18 include allowable subject matter and would be allowable if rewritten to overcome the rejections under Section 112, first and second paragraph, as described above. Claims 17 and 18 have been so rewritten and are now believed to be allowable.

In view of the foregoing amendments and remarks, Applicant respectfully requests favorable reconsideration and early passage to issue of the present application.

Applicant's undersigned attorney may be reached in our New York office by telephone at (212) 218-2100. All correspondence should continue to be directed to our below listed address.

Respectfully submitted,



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